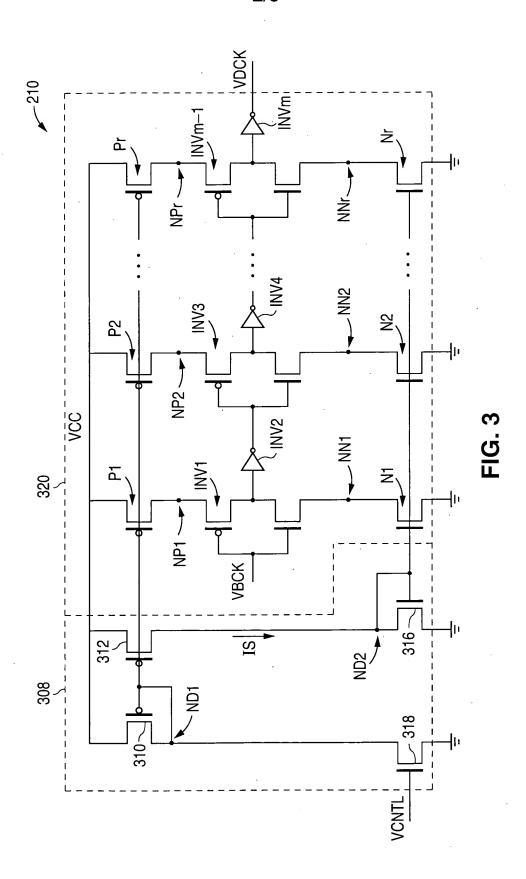


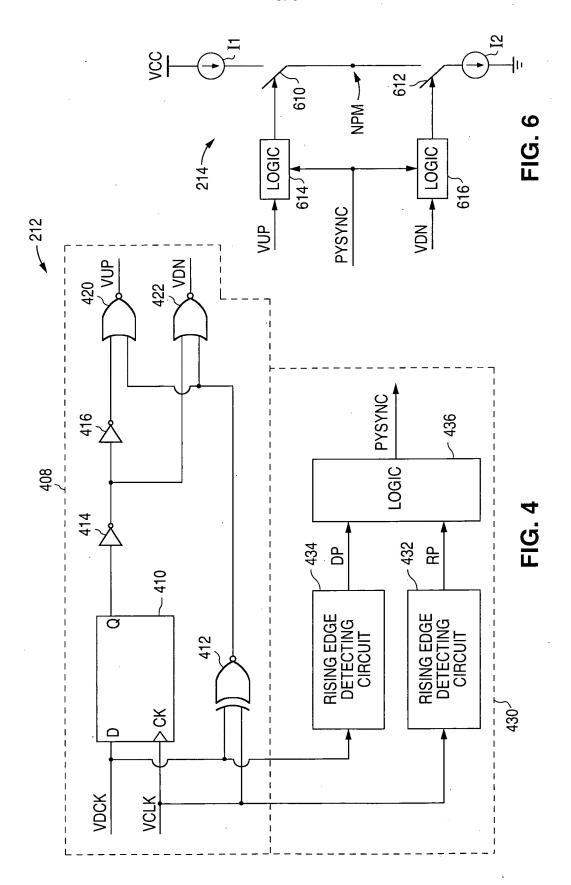
100 VCDL VCLK FILTER VPM CHARGE PUMP NO PHASE DETECTOR

FIG. 1 (PRIOR ART)

7 200 VDCK VBCK DEL SEL 0 210 VCDL 218 SEL VCNTL DEL 222 FILTER 216 CONTROL VPM CHARGE PUMP DEL n-1 <sup>2</sup> 214 VST VRST **PYSYNC** 226~| SET / RESE PHASE DETECTOR VUP. DEL SEL n-VDN SEL n-1 212

FIG. 2





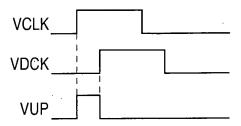


FIG. 5A

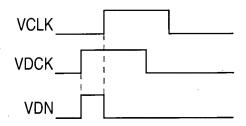


FIG. 5B

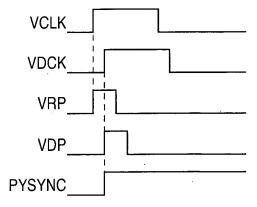


FIG. 5C

